

Agenda

1.Day

Start 9:00 AM

UB1

- Introduction / Greetings
- Windows System Architecture

Pause

UB2

- Windows LEGACY Driver Architecture
- Windows WDM Driver Architecture

Lunch

UB3

- WDK Design and Reference
- WDM Developing Environment
- Driver Objects

Pause

UB4

- Application <-> Device communication
- Application <-> Device Data Transfer



Agenda

2.Day

Start 9:00 AM

UB1

- Kernel Mode Driver Framework (KMDF)
- Supported Devices

Pause

UB2

- KMDF Objects
- KMDF Object Types

Lunch

UB3

- KMDF I/O Model
- KMDF Dispatch Types

Pause

UB4

- KMDF DriverEntry
- KMDF Developing Environment



Agenda

3.Day

Start 9:00 AM

UB₁

- (Non-) PNP Bus Systems
- Device Parameters
- Ressource Assignment

Pause

UB2

- IO-Port Access
- Physical Memory

Lunch

UB3

- DMA Methods
- DMA Transfer

Pause

UB4

• Interrupt Management



Agenda

4.Day

Start 9:00 AM

UB1

• User Mode Driver Framework (UMDF)

Pause

UB2

- UMDF Stack Architecture
- UMDF USB Filterstack

Lunch

UB3

- UMDF COM Interface
- UMDF COM Objects

Pause

UB4

- UMDF DLL Entry Point
- UMDF Developing Environment



Agenda

5.Day

Start 9:00 AM

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- Timer Handling
- Event Handling

Pause

UB2

- PLUG & PLAY Management
- POWER-Management

Lunch

UB3

• INF-Files

Pause

UB4

Code Signing